BIODATA

1. General Information:

First Name : CHIDANAND S. KUSUR

Father's & Mother's Name: Sri. Sidramappa M. Kusur & Smt. Ambuja S. Kusur

Designation : Asst. Professor

Date of Birth : 1st June 1975

Nationality : Indian

Office Address : Department of Computer Science and Engineering

B. L. D. E. A's V. P. Dr. P. G. Halakatti College

of Engineering and Technology, Ashram Road,

VIJAYAPUR – 586 103, Karnataka, India.

Mobile : +91-9739762682

Email : cs.kusur@gmail.com

Blog : www.cskusur.blogspot.in

Present Address: S/o. S. M. Kusur

Adarsh Nagar, H.No.72, MIG-II

Near B.L.D.E.A's Engg. College Compound,

BIJAPUR - 586 103.

2. Educational Qualifications:

Sl. No.	Degree	University/Board	Year	Class
1.	M.Tech., CSE	Visvesvaraya Technological University, Belagavi	2014	First Class 66 %
2.	A.M.I.E., CSE	Institution of Engineers (India), Kolkata.	2009	First Class, 63 %
3.	Diploma	Board of Technical Education, Bangalore.	1996	Second Class 58 %
4.	S.S.L.C.	Karnataka Secondary Education Examination Board, Bangalore	1991	FCD 73%



3. Teaching Experience: 7 Years

Sl. No.	Name of Organization / Institute	Designation	Duration
1.	B.L.D.E.A's V. P. Dr. P. G. Halakatti College of Engineering and Technology, Bijapur	Asst. Professor	02.08.2010 To Till date

4. Area of Interest:

Programming Languages, Parallel Programming, Design and analysis of algorithms, Computer Architecture & High Performance Computing.

5. Workshop/FDP/Seminar/STTP/Tutorial Attended:

Sl. No.	Topic Name	Organized by	Duration
1	Five Days Pre Placement Training	BLDEA's CET, Vijayapur, Seventh Sense Talent Solutions, Bangalore	24 th to 28 th January 2017
2	Ten Days FDP on "Parallel Computing"	National Institution of Technology, Warangal	11 th to 20 th February 2016
3	One Week FDP on "Research Methodology and Issues"	BLDEA's CET, Bijapur.	8 th to 12 th June,2015
4	TWO - WEEK ISTE STTP on "Pedagogy for Effective use of ICT in Engineering Education"	Remote Centre: Walchand Institute of Technology, Solapur.	5 th Jan -7 th Jan and 19 th – 21 st Jan 2015
5.	Two Days FDP on "Networking 101 and Network Access Control"	BLDEA's CET, Bijapur.	18-09-2014 to 19-09-2014
6.	One Day workshop on "Computer Programming Laboratory"	B. V. V. Sangha's, BGMIT, Mudhol.	30-07-2014
7	Three Days National Workshop on "Massive Parallel Computing"	KLE's B.V.B., Hubli	21-11-2013 to 23-11-2013
8	Two Days workshop on "Research Issues in Data Mining"	BLDEA's CET, Bijapur.	26-10-2013 to 27-10-2013
9	A workshop on "Parallel Programming"	Sahyadri College of Engineering and Management, Mangalore-07	17 th Sept., 2010

6. List of Courses Taught:

- Programming in C and Data Structures
- Design and Analysis of Algorithms and
- Unix and Shell Programming

7. List of U.G. Projects Guided:

Academic	Title
Year	
2016-17	Robust blur kernel estimation for license plate images from fast moving vehicles
2014-15	An Energy Balanced Routing Method Based on Forward Aware Factor for WSN
2014-15	Informative State Based Video State Communication
2013-14	E-Publisher
2013-14	Anchor – A versatile and efficient framework for resource management in the cloud
2012-13	Port Knocking
2011-12	Neural Network
2010-11	Karnataka Crop Database Management System
2010-11	Time Management Sheet

List of P.G. Projects Guided:

Academic Year				Tit	le		
2016-17	Artificial	Neural	Network	Based	Classification	for	Mammographic
	Microcalc	ification (Clusters				

List of Mini Project Guided for U. G. Students

Academic Year	Title
	Automated Annual Increment Form

List of Mini Project Guided for P. G. Students

Academic Year	Title
2016-17	Master time table management system

List of U.G. Seminars Guided:

Academic	Title
Year	
2016-17	1.Android based attendance management
	2. Fraud detection in cried card using hidden MARKOV model
	3. Dynamic routing
	4. Applications of internet of Things with motion sensing on smart house
2014-15	1. Zforce touch screen technology
	2. Vector valued image processing by parallel level sets
	3. Mobile phone cloning
	4. Digital watermarking techniques, applications and attack

8. Number of Workshop/Seminar Organized: 04

- One Day Bridge Course on "C Programming Concepts" for III sem. of CSE students, 13th Sept., 2013.
- One Day Seminar on "Computer Programming Lab" for faculty and instructional staff members of CSE department, 7th August, 2014.
- Organizing Committee Member for One Week ISTE–PTU sponsored Faculty Development Programme (FDP) on "Wavelets and Applications", June 9-13th 2014.
- Organizing Committee Member for One Week FDP on "Research Methodology and Issues", 8th to 12th June, 2015.

9. Paper Publications: 02

- [1]. Vilas H. Naik & Chidanand S. Kusur, "Analysis of performance enhancement on graphic processor based heterogeneous architecture: A CUDA and MATLAB experiment", IEEE National Conference on Parallel Computing Technologies (PARCOMPTECH), 2015, 19-20 Feb. 2015, pp 1-5, ISPN 978-1-4799-6916-6.
- [2]. Chandrakala Honnutagi & Chidanand S. Kusur, "Artificial Neural Network Based Classification for Mammographic Microcalcification Clusters", International Research Journal of Engineering and Technology (IRJET), Vol. 4, Issue-10 Oct-2017,pp. 71-81, e-ISSN: 2395-0056.