

# **BIO-DATA**

## **GENERAL INFORMATION:**

**Full Name:** Prof. Ananda S. Hiremath

**Designation:** Assistant Professor

**Department:** Department of Computer Science and Engineering

**Office Address:** Department of Computer Science and Engineering, B.L.D.E.A's V. P. Dr. P. G. Halakatti College of Engineering and Technology, Ashram Road, VIJAYPUR – 586 103, Karnataka, India.

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## **EDUCATIONAL QUALIFICATIONS:**

- Completed M.Tech in VLSI Design and Embedded Systems at Department of P.G. Studies, Visvesvaraya Technological University, Belgaum.
- Completed B.E in Electronics and Communication at BLDEA's V.P. Dr. P. G. Halakatti College of Engineering & Technology, Vijaypur.

## **TEACHING EXPERIENCE:**

- Working as Assistant Professor in BLDEA's V.P. Dr. P. G. Halakatti College of Engineering & Technology, Vijaypur, from January 2012 to till today.
- Worked as Lecturer in BLDEA's V.P. Dr. P. G. Halakatti College of Engineering & Technology, Vijaypur, from August 2009 to December 2011.

## **AREA OF INTEREST:**

- Embedded Systems, Image Processing, ASICs Design

## **PUBLISHED PAPERS:**

1. Somanagouda M Moolimani, A. S. Hiremath, "Detecting and Providing Solutions to Privacy Issues in Social Media," International Research Journal of Engineering and Technology (IRJET), Volume 04, Issue 06, June 2017.
2. Heena M. Sangtrash , Anand S. Hiremath, "Review on IoT for Indian Farmers," International Journal of Scientific Research in Computer Science, Engineering and Information Technology (IJSRCSEIT), Volume 2, Issue 3, May 2017.
3. Priyanka Sajjan, Prof. Anand S. Hiremath, "Efficient knowledge sharing in multi-user scenario," International Research Journal of Engineering and Technology (IRJET), Volume 03, Issue 11, November-2016.

4. Tarannum A. Kumasagi, Prof. Anand S. Hiremath, "An Effective Recommendation Technique in Tourism," International Research Journal of Engineering and Technology (IRJET), Volume 03, Issue 05, May-2016.
5. Nandakishor Jahagirdar, Prof. Anand Hiremath, Senior Scientist Sudeesh B, "Simultaneous Mapping and Navigation For Rendezvous in Space Applications," 2nd International Conference on Applied and Theoretical Computing and Communication Technology (iCATccT), IEEE, June 2016.
6. Anand Hiremath, Bhagyashree Biradar, Gouramma V.G, Shruti Patil, Surekha Pawar, "Robust Face-Name Identification from Streaming Video using Error Correcting Graph Matching Algorithm," International Conference on Emerging Trends in Engineering Sciences, JCE, Belgaum, pp. 455-460, April 2015.
7. Ms. Shrilata Savakar, Prof. Anand Hiremath, "Survey Paper on Enhanced Adaptive Acknowledgment for MANETs," International Conference on Emerging Trends in Technology, Science and Upcoming Research in Computer Science, vol 03, issue 01, 2015
8. Ananda S. Hiremath, Shivaputra S. Panchal, Shriharsha S. Veni, "Providing Security for Data Storage in Cloud through Third Party Auditor," International Journal of Advanced Research in Computer Science and Software Engineering (IJARCSSE), vol. 4, issue 7, pp. 379-384, 2014.

#### **WORKSHOP/FDP/SEMINAR/TUTORIAL ATTENDED:**

1. Participated in one day workshop on "ARM Controller & Its Applications" conducted at BLDEA's CET, Vijayapur on 07th February 2017.
2. Participated in a One-Weeks FDP on "Research Literacy and Trends in Scholarly Communications" conducted by BLDEA's CET, Vijayapur, from 23rd to 28th January 2017.
3. Participated in one day FDP on "ARM Controller" conducted by KLE IT, Hubli, on 28th December 2016.
4. Participated in a One-Weeks STTP ISTE sponsored Workshop on "Recent Advances in Electronics and Communication" conducted by BLDEA's CET, Vijayapur, from 04th to 09th April 2016.
5. Participated in a Two-Weeks ISTE sponsored Workshop on "Effective Mentoring of Online Students in Signals and System Part-2" conducted by IIT, Bombay, from 11th June 2015 to 17th August 2015.
6. Participated in a One-Weeks FDP on "Research Methodology and Issues" BLDEA's V.P. Dr. P. G. Halakatti College of Engineering & Technology, Vijaypur from 8th to 12th June 2015.
7. Participated in a Two-Weeks ISTE sponsored Workshop on "Effective Mentoring of Online Students in Signals and System Part-1" conducted by IIT, Bombay, from 12th March 2015 to 20th May 2015.
8. Participated in a Two-Weeks ISTE sponsored Workshop on "Pedagogy for Effective Use of ICT In Engineering Education" conducted by IIT, Bombay at WIT, Solapur from 2nd to 31st January 2015.
9. Participated in a Two-Weeks ISTE sponsored Workshop on "Control System" conducted by IIT, Kharagpur at WIT, Solapur from 2nd to 12th December 2014.

10. Participated in Intel Corp. sponsored FDP on “Embedded Computing Applications” at BLDEA’s V.P. Dr. P. G. Halakatti College of Engineering & Technology, VIJAYPUR from 24<sup>th</sup> to 26<sup>th</sup> June 2014.
11. Participated in ISTE-PTU sponsored one week FDP on “Wavelets and Applications” at BLDEA’s V.P. Dr. P. G. Halakatti College of Engineering & Technology, VIJAYPUR from 09<sup>th</sup> to 14<sup>th</sup> June 2014.
12. Attended in one day seminar on “Recent Advances in Computer Application” organized by Karnataka State Women’s University, VIJAYPUR on 27<sup>th</sup> February 2013.
13. Participated one week VTU-VGST FDP on “Video and Image Processing Design using LabVIEW/DSP” at Oxford College of Engineering, Bangalore, from 13<sup>th</sup> to 17<sup>th</sup> March 2012
14. Participated in AICTE sponsored one week FDP on “LabVIEW and its Application in Engineering” at BMS College of Engineering, Bangalore from 11<sup>th</sup> to 16<sup>th</sup> July 2011.
15. Attended a TIFAC-DST sponsored one day workshop on “Intellectual Property Rights” jointly organized by KSCST, Bangalore at BLDEA’s V.P. Dr. P. G. Halakatti College of Engineering & Technology, VIJAYPUR, conducted on 25 February 2011.
16. Attended three day FDP on “Advanced Computer Network and Simulator” organized by REVA Institute of Technology and Management, Bangalore and sponsored by VTI-Sision Group on Science and Technology (VTU-VGST), GoK, from 27<sup>th</sup> to 29<sup>th</sup> January 2011.
17. Participated in a “MISSION 10X” workshop organized by WIPRO Technologies, Bangalore and ISTE conducted at Basaveshwar Engineering College, Bagalkot from 2<sup>nd</sup> to 6<sup>th</sup> August 2010.
18. Attended one day workshop on “Aerospace Science and Technologies” organized by CSIR-National Aerospace Laboratories (NAL), Bangalore and BLDEA’s V.P. Dr. P. G. Halakatti College of Engineering & Technology, VIJAYPUR during 19<sup>th</sup> to 21<sup>st</sup> February 2010.

### **LIST OF COURSES TAUGHT:**

1. Analog and Digital Electronics
2. Electronics Circuits
3. Logic Design
4. Microprocessors and Microcontroller
5. Computer Organization
6. Embedded Computing System
7. Internet of Things
8. Software Testing

## **WORKSHOP/SEMINAR ORGANIZED:**

1. One day seminar on “Internet of Things and its Application”.
2. One Week Entrepreneurship Programme on “Entrepreneurship Awareness Camp” from 7 to 9 September 2016.
3. Two Days “.NET framework” for students from 29 to 30 August 2016.
4. Two Days “Android Workshop and Championship” for students from 29 to 30 August 2015.
5. One Week Faculty Development Programme (FDP) on “Research Methodology and Issues” from 8 to 12 June 2015.
6. One Week ISTE-PTU sponsored Faculty Development Programme (FDP) on “Wavelets and Applications” from 9 to 13 June 2014.
7. Two Days Workshop on “National Networks Security Championship NNSC-2014” for students from 15 to 16 February 2014.
8. Two Days Faculty Development Programme (FDP) on “Research Issues in Data Mining RIDM-2013” from 26 to 27 October 2013.

## **INVITED AS GUEST LECTURES:**

1. On “Microprocessors” at SECAB Institute of Engineering and Technology, Vijaypur from 10th to 11th April 2014.
2. On “Microprocessors” at Department of BCA, ACP Commerce College, Vijaypur during January 2011.
3. On “Basic Electrical and Electronics” at Department of BCA, ACP Commerce College, Vijaypur during August 2012.

## **MEMBERSHIPS AND ACTIVITIES IN PROFESSIONAL ASSOCIATIONS:**

1. Life Member of the ISTE (Membership No: LM 98119).

## **OTHER ACHIEVEMENTS:**

1. Qualified in the **UGC-NET June 2013 Exam** (Electronic Science)

**Ananda S. Hiremath**